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Application No. 09/630,883

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Khosrow Golshan
Title: METHOD AND APPARATUS
FOR OPTICAL PROCESSING
Appl. No.: 09/630,883
Filing Date: 08/02/2000
Examiner: Chang, A.
Art Unit: 2872

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<u>Chris Escanilla</u> (Printed Name)	
<u>Chris Escanilla</u> (Signature)	
2/22/02 (Date of Deposit)	

Commissioner for Patents
Box NON-FEE AMENDMENT
Washington, D.C. 20231

Sir:

This communication is responsive to the Office Action dated December 3, 2001, concerning the above-referenced patent application.

Please amend the application as follows:

In the Claims:

Please cancel claims 2, 3, 12, 16, 32, and 37, without prejudice.

Marked up versions of the replacement claims below have been included in Appendix A of this Reply and Amendment in accordance with 37 C.F.R. § 1.121.

Sub B1
A

1. (Once Amended) An optical logic circuit, comprising:
a substrate comprising a first material;
an optical layer overlaying the substrate at least partially comprising a second material, the optical layer configured to provide a plurality of optical pathways, at least one optical pathway configured to transmit an optical bias signal, at least one optical pathway configured to provide an optical input signal, and at least one optical pathway configured to provide an optical output signal; and